

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	19476	charge adj pump	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2006/09/06 14:42
L2	67743	voltage adj divider	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2006/09/06 14:42
L3	1671	1 and 2	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2006/09/06 14:43
L4	2419	365/226.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2006/09/06 14:51
L5	84	3 and 4	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2006/09/06 14:49
L6	15855	voltage adj detector	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2006/09/06 14:49
L7	158133	voltage near reference	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2006/09/06 14:49
L8	988	2 and 6 and 7	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2006/09/06 14:49
L9	96	1 and 8	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2006/09/06 14:49
L10	9	4 and 9	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2006/09/06 14:49
L11	78745	"365"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2006/09/06 14:51

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L12	297	3 and 11	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2006/09/06 15:02
L13	2176514	switch\$3	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2006/09/06 15:03
L14	4580	2 near5 13	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2006/09/06 15:06
L15	17	4 and 14	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2006/09/06 15:03
L16	9756	vpp	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2006/09/06 15:06
L17	10	14 same 16	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2006/09/06 15:07
L18	141	1 and 14	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2006/09/06 15:10
L19	2106	365/189.09.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2006/09/06 15:10
L20	473	1 and 19	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2006/09/06 15:10
L21	76	2 and 20	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2006/09/06 15:14
L22	37062	voltage adj generator	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2006/09/06 15:14
L23	1026	1 same 22	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2006/09/06 15:14

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L24	198	2 and 23	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2006/09/06 15:16
L25	30	19 and 24	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2006/09/06 15:14

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L1	6035	refresh adj rate	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2006/09/06 13:06
L2	7840	mode adj register	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2006/09/06 13:07
L3	383	1 and 2	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2006/09/06 13:07
L4	3253024	temperature	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2006/09/06 13:07
L5	295	3 and 4	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2006/09/06 13:07
L6	3295	365/222.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2006/09/06 13:07
L7	21	5 and 6	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2006/09/06 13:11
L8	46	kim-jung-pill.in.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2006/09/06 13:11
L9	70	oh-jong-hoon.in.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2006/09/06 13:11
L10	5	bowyer-stephen.in.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2006/09/06 13:11

Day : Wednesday

Date: 9/6/2006
Time: 16:42:53

PALM INTRANET
Inventor Name Search Result

Your Search was:

Last Name = KIM

First Name = JUNG

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<u>06692786</u>	Not Issued	161	01/17/1985	ADJUSTABLE GOLF CLUB	KIM, JUNG N.
<u>08999257</u>	<u>5953279</u>	150	12/29/1997	FUSE OPTION CIRCUIT FOR MEMORY DEVICE	KIM, JUNG P
<u>07615879</u>	<u>5130581</u>	150	11/20/1990	SENSE AMPLIFIER IN A DYNAMIC RAM HAVING DOUBLE POWER LINES	KIM, JUNG P.
<u>08096025</u>	Not Issued	161	07/02/1993	WRITE ENABLE (WE) BUFFER PROTECTION CIRCUIT	KIM, JUNG P.
<u>08431429</u>	Not Issued	166	05/01/1995	SEMICONDUCTOR DEVICE	KIM, JUNG P.
<u>08580200</u>	Not Issued	166	12/28/1995	LOW VOLTAGE TRANSISTOR LOGIC AND HIGH SPEED INPUT/OUTPUT VOLTAGE SELECTION CIRCUIT	KIM, JUNG P.
<u>08770818</u>	<u>5834974</u>	150	12/20/1996	DIFFERENTIAL AMPLIFIER WITH REDUCED CURRENT CONSUMPTION	KIM, JUNG P.
<u>08777224</u>	<u>5818790</u>	150	12/27/1996	METHOD FOR DRIVING WORD LINES IN SEMICONDUCTOR MEMORY DEVICE	KIM, JUNG P.
<u>08906005</u>	<u>5767583</u>	150	08/04/1997	SEMICONDUCTOR CHIP I/O AND POWER PIN ARRANGEMENT	KIM, JUNG P.
<u>08939354</u>	Not Issued	161	09/29/1997	METHOD FOR MANUFACTURING PHOTOGRAPHIC FILM OF EXCELLENT ANTISTATICITY	KIM, JUNG PAK
<u>08958555</u>	Not Issued	161	10/29/1997	METHOD FOR MANUFACTURING PHOTOGRAPHIC FILM OF EXCELLENT ANTISTATICITY	KIM, JUNG PAK

<u>09853361</u>	Not Issued	161	05/10/2001	Magnetized accupressure apparatus	KIM, JUNG PIL
<u>10337606</u>	<u>6774691</u>	150	01/07/2003	HIGH RESOLUTION INTERLEAVED DELAY CHAIN	KIM, JUNG PILL
<u>10672246</u>	<u>6934645</u>	150	09/25/2003	TEMPERATURE SENSOR SCHEME	KIM, JUNG PILL
<u>10681647</u>	<u>7038523</u>	150	10/08/2003	VOLTAGE TRIMMING CIRCUIT	KIM, JUNG PILL
<u>10690358</u>	Not Issued	161	10/21/2003	Random access memory having self-adjusting off-chip driver	KIM, JUNG PILL
<u>10692119</u>	<u>6847566</u>	150	10/23/2003	METHOD AND CIRCUIT CONFIGURATION FOR MULTIPLE CHARGE RECYCLING DURING REFRESH OPERATIONS IN A DRAM DEVICE	KIM, JUNG PILL
<u>10696920</u>	Not Issued	164	10/30/2003	DELAY LOCKED LOOP PHASE BLENDER CIRCUIT	KIM, JUNG PILL
<u>10706146</u>	<u>6839288</u>	150	11/12/2003	LATCH SCHEME WITH INVALID COMMAND DETECTOR	KIM, JUNG PILL
<u>10716749</u>	Not Issued	71	11/19/2003	Internal voltage generator with temperature control	KIM, JUNG PILL
<u>10716762</u>	<u>7009904</u>	150	11/19/2003	BACK-BIAS VOLTAGE GENERATOR WITH TEMPERATURE CONTROL	KIM, JUNG PILL
<u>10739097</u>	<u>7064586</u>	150	12/19/2003	INPUT BUFFER CIRCUIT INCLUDING REFERENCE VOLTAGE MONITORING CIRCUIT	KIM, JUNG PILL
<u>10739398</u>	Not Issued	93	12/19/2003	OSCILLATOR WITH TEMPERATURE CONTROL	KIM, JUNG PILL
<u>10744807</u>	Not Issued	30	12/23/2003	Temperature compensated delay signals	KIM, JUNG PILL
<u>10808190</u>	Not Issued	41	03/24/2004	Temperature sensor scheme	KIM, JUNG PILL
<u>10826840</u>	<u>7061305</u>	150	04/16/2004	THRESHOLD VOLTAGE DETECTOR FOR PROCESS EFFECT COMPENSATION	KIM, JUNG PILL
<u>10867896</u>	Not Issued	41	06/15/2004	Comparator using differential amplifier with reduced current consumption	KIM, JUNG PILL
<u>10881598</u>	Not Issued	61	06/30/2004	Digital duty cycle corrector	KIM, JUNG PILL

<u>10967768</u>	Not Issued	89	10/18/2004	DQS for data from a memory array	KIM, JUNG PILL
<u>10973389</u>	7046060	150	10/27/2004	METHOD AND APPARATUS COMPENSATING FOR FREQUENCY DRIFT IN A DELAY LOCKED LOOP	KIM, JUNG PILL
<u>11032459</u>	Not Issued	71	01/10/2005	Duty cycle corrector	KIM, JUNG PILL
<u>11048185</u>	Not Issued	71	02/01/2005	Duty cycle corrector	KIM, JUNG PILL
<u>11247538</u>	Not Issued	30	10/11/2005	Duty cycle corrector	KIM, JUNG PILL
<u>11442842</u>	Not Issued	20	05/30/2006	Duty cycle corrector	KIM, JUNG PILL
<u>08864471</u>	5963077	150	05/28/1997	AUTO MODE SELECTOR	KIM, JUNG PILL
<u>08893542</u>	5818783	150	07/11/1997	AUTOMATIC MODE SELECTION CIRCUIT FOR SEMICONDUCTOR MEMORY DEVICE	KIM, JUNG PILL
<u>08960228</u>	5973521	150	10/29/1997	SEMICONDUCTOR DEVICE FOR AUTOMATICALLY DETECTING EXTERNAL INTERFACE VOLTAGE	KIM, JUNG PILL
<u>09080411</u>	5940330	150	05/18/1998	SYNCHRONOUS MEMORY DEVICE HAVING A PLURALITY OF CLOCK INPUT BUFFERS	KIM, JUNG PILL
<u>09206318</u>	6041009	150	12/07/1998	APPARATUS FOR STABILIZING AN ANTIFUSE USED FOR A MEMORY DEVICE	KIM, JUNG PILL
<u>09221344</u>	6094380	150	12/28/1998	MEMORY DEVICE WITH A DATA OUTPUT BUFFER AND THE CONTROL METHOD THEREOF	KIM, JUNG PILL
<u>60346897</u>	Not Issued	159	01/11/2002	Increasing a refresh period in a semiconductor memory device	KIM, JUNG PILL
<u>07723593</u>	5200589	250	07/01/1991	A MICROWAVE OVEN HAVING A FAN MOTOR ROTATION CONTROL FUNCTION	KIM, JUNG R.
<u>09731197</u>	6792163	150	12/06/2000	METHOD AND APPARATUS FOR SEARCHING, BROWSING AND SUMMARIZING MOVING	KIM, JUNG RIM

				IMAGE DATA USING FIDELITY OF TREE-STRUCTURED MOVING IMAGE HIERARCHY	
09911293	Not Issued	41	07/23/2001	System and method for indexing, searching, identifying, and editing portions of electronic multimedia files	KIM, JUNG RIM
10361794	Not Issued	41	02/10/2003	Rapid production of reduced-size images from compressed video streams	KIM, JUNG RIM
10902969	Not Issued	71	07/30/2004	Method and apparatus for searching, browsing and summarizing moving image data using fidelity for tree-structured moving image hierarchy	KIM, JUNG RIM
11069750	Not Issued	30	03/01/2005	Delivery and presentation of content-relevant information associated with frames of audio-visual programs	KIM, JUNG RIM
11069767	Not Issued	30	03/01/2005	Generating, transporting, processing, storing and presenting segmentation information for audio-visual programs	KIM, JUNG RIM
11071894	Not Issued	30	03/03/2005	Delivering and processing multimedia bookmark	KIM, JUNG RIM
11071895	Not Issued	30	03/03/2005	Fast metadata generation and delivery	KIM, JUNG RIM

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PALM INTRANET
Inventor Name Search Result

Your Search was:

Last Name = OH

First Name = JONG

Application#	Patent#	Status	Date Filed	Title	Inventor Name
11112833	Not Issued	71	04/22/2005	Lactic acid bacteria inhibiting halitosis	OH, JONG SUK
09014436	6036952	150	01/27/1998	NOVEL LACTIC ACID BACTERIA INHIBITING THE FORMATION OF DENTAL PLAQUE IN THE MOUTH	OH, JONG SUK
07500304	Not Issued	166	03/28/1990	MICROORGANISM CAPABLE OF PRODUCING L-LYSINE AND METHOD FOR PRODUCING L-LYSINE THEREBY	OH, JONG WON
07851120	5268293	150	03/16/1992	MICROORGANISM CAPABLE OF PRODUCING L-LYSINE AND METHOD FOR PRODUCING L-LYSINE THEREBY	OH, JONG WON
11020161	Not Issued	20	12/27/2004	Scan image correction device and method thereof	OH, JONG-CHEOL
11293456	Not Issued	20	12/02/2005	Detecting and eliminating method for ghosting effect of LCD	OH, JONG-DAE
08700449	5889148	150	08/27/1996	NOVEL ANTIBIOTIC PEPTIDES	OH, JONG-EUN
09312405	6140363	150	05/14/1999	USE OF ROSMARINIC ACID AND DERIVATIVES THEREOF AS AN IMMUNOSUPPRESSANT OR AN INHIBITOR OF SH2-MEDIATED PROCESSES	OH, JONG-EUN
10211531	Not Issued	41	08/05/2002	Precise position control apparatus and precise position control method using the same	OH, JONG-HAN
11127198	Not Issued	30	05/12/2005	Substrate inspecting apparatus and control method thereof	OH, JONG-HAN
11146061	Not Issued	25	06/07/2005	Display apparatus and control method thereof	OH, JONG-HAN
11168395	Not	30	06/29/2005	Jetting performance tester usable	OH, JONG-HAN

	Issued			with jetting heads	
<u>11206037</u>	Not Issued	30	08/18/2005	Aligning apparatus	OH, JONG-HAN
<u>07746902</u>	<u>5121027</u>	150	08/19/1991	OXIDE-COATED CATHODE FOR CRT AND MANUFACTURING METHOD THEREOF	OH, JONG-HO
<u>07936783</u>	<u>5236382</u>	250	08/28/1992	METHOD FOR MANUFACTURING AN IMPREGNATED CATHODE STRUCTURE	OH, JONG-HO
<u>08401981</u>	<u>5717032</u>	150	03/10/1995	FILMING COMPOSITE FOR CATHODE RAY TUBE	OH, JONG-HO
<u>08851090</u>	<u>5853796</u>	150	05/05/1997	METHOD FOR MANUFACTURING A SCREEN USING THE SAME	OH, JONG-HO
<u>07213177</u>	Not Issued	166	06/29/1988	CEPHEM DERIVATIVES AND A PROCESS FOR THEIR MANUFACTURE	OH, JONG-HOON
<u>07397329</u>	Not Issued	161	08/24/1989	CEPHEM DERIVATIVES AND A PROCESS FOR THEIR MANUFACTURE	OH, JONG-HOON
<u>08367551</u>	<u>5594765</u>	150	01/03/1995	INTERLEAVED AND SEQUENTIAL COUNTER	OH, JONG-HOON
<u>08828817</u>	<u>5875149</u>	150	02/06/1997	WORD LINE DRIVER FOR SEMICONDUCTOR MEMORIES	OH, JONG-HOON
<u>08954248</u>	<u>5936432</u>	150	10/20/1997	HIGH SPEED LOW POWER AMPLIFIER CIRCUIT	OH, JONG-HOON
<u>09182943</u>	<u>6011746</u>	150	10/29/1998	WORD LINE DRIVER FOR SEMICONDUCTOR MEMORIES	OH, JONG-HOON
<u>09571635</u>	Not Issued	161	05/15/2000	Dta strobe circuit for semiconductor memories	OH, JONG-HOON
<u>09588994</u>	<u>6275441</u>	150	06/06/2000	Data input/output system for multiple data rate memory devices	OH, JONG-HOON
<u>09607802</u>	<u>6501670</u>	150	06/30/2000	HIGH SPEED MEMORY ARCHITECTURE AND BUSING	OH, JONG-HOON
<u>09608674</u>	<u>6496032</u>	150	06/29/2000	METHOD AND STRUCTURE FOR EFFICIENTLY PLACING AND INTERCONNECTING CIRCUIT BLOCKS IN AN INTEGRATED CIRCUIT	OH, JONG-HOON
<u>09654147</u>	<u>6445641</u>	150	09/01/2000	MEMORY DEVICE WITH TIME SHARED DATA LINES	OH, JONG-HOON
<u>09660860</u>	<u>6765976</u>	150	09/13/2000	DELAY-LOCKED LOOP FOR DIFFERENTIAL CLOCK	OH, JONG-HOON

				SIGNALS	
<u>09757023</u>	Not Issued	161	01/08/2001	Delay-locked loop for differential clock signals	OH, JONG-HOON
<u>09823325</u>	<u>6606272</u>	150	03/29/2001	METHOD AND CIRCUIT FOR PROCESSING OUTPUT DATA IN PIPELINED CIRCUITS	OH, JONG-HOON
<u>09863151</u>	<u>6529428</u>	150	05/22/2001	MULTI-BIT PARALLEL TESTING FOR MEMORY DEVICES	OH, JONG-HOON
<u>10672118</u>	<u>6909660</u>	150	09/26/2003	RANDOM ACCESS MEMORY HAVING DRIVER FOR REDUCED LEAKAGE CURRENT	OH, JONG-HOON
<u>10672120</u>	<u>6985398</u>	150	09/26/2003	MEMORY DEVICE HAVING MULTIPLE ARRAY STRUCTURE FOR INCREASED BANDWIDTH	OH, JONG-HOON
<u>10672243</u>	Not Issued	41	09/25/2003	Refresh rate adjustment	OH, JONG-HOON
<u>10675549</u>	<u>6996016</u>	150	09/30/2003	ECHO CLOCK ON MEMORY SYSTEM HAVING WAIT INFORMATION	OH, JONG-HOON
<u>10689954</u>	Not Issued	20	10/21/2003	Well-matched echo clock in memory system	OH, JONG-HOON
<u>10704091</u>	<u>7095669</u>	150	11/07/2003	REFRESH FOR DYNAMIC CELLS WITH WEAK RETENTION	OH, JONG-HOON
<u>10715812</u>	Not Issued	41	11/18/2003	Method and circuit configuration for refreshing data in a semiconductor memory	OH, JONG-HOON
<u>10716749</u>	Not Issued	71	11/19/2003	Internal voltage generator with temperature control	OH, JONG-HOON
<u>10766428</u>	<u>7042777</u>	150	01/28/2004	MEMORY DEVICE WITH NON-VARIABLE WRITE LATENCY	OH, JONG-HOON
<u>10804461</u>	<u>7027344</u>	150	03/18/2004	HIGH-SPEED SEMICONDUCTOR MEMORY HAVING INTERNAL REFRESH CONTROL	OH, JONG-HOON
<u>10805024</u>	<u>6982911</u>	150	03/18/2004	MEMORY DEVICE WITH COMMON ROW INTERFACE	OH, JONG-HOON
<u>10820292</u>	Not Issued	93	04/08/2004	MULTICHIP PACKAGE WITH CLOCK FREQUENCY ADJUSTMENT	OH, JONG-HOON
<u>10832117</u>	<u>7042786</u>	150	04/26/2004	MEMORY WITH ADJUSTABLE ACCESS TIME	OH, JONG-HOON
<u>10870476</u>	Not	41	06/16/2004	SEMICONDUCTOR MEMORY	OH, JONG-HOON

	Issued			WITH 2-PHASE INTERNAL REFRESH OPERATION	
10987812	Not Issued	30	11/12/2004	Memory access using multiple sets of address/data lines	OH, JONG-HOON
10992826	Not Issued	90	11/19/2004	TWIN-CELL BIT LINE SENSING CONFIGURATION	OH, JONG-HOON
11039293	Not Issued	30	01/20/2005	Signal redistribution using bridge layer for multichip module	OH, JONG-HOON
11108179	Not Issued	41	04/18/2005	Redundancy circuits for semiconductor memory	OH, JONG-HOON

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**PALM INTRANET****Inventor Name Search Result**

Your Search was:

Last Name = BOWYER

First Name = STEPHEN

Application#	Patent#	Status	Date Filed	Title	Inventor Name
10265964	6920523	150	10/07/2002	BANK ADDRESS MAPPING ACCORDING TO BANK RETENTION TIME IN DYNAMIC RANDOM ACCESS MEMORIES	BOWYER, STEPHEN
10716749	Not Issued	71	11/19/2003	Internal voltage generator with temperature control	BOWYER, STEPHEN
10739398	Not Issued	93	12/19/2003	OSCILLATOR WITH TEMPERATURE CONTROL	BOWYER, STEPHEN
11059899	Not Issued	30	02/17/2005	Error correction circuit and method	BOWYER, STEPHEN
11270178	Not Issued	30	11/09/2005	Method and apparatus for reducing standby current in a dynamic random access memory during self refresh	BOWYER, STEPHEN
07058028	4772078	150	06/04/1987	DRAWER INTERLOCKING MEANS FOR STORAGE CABINET	BOWYER, STEPHEN M.
07225351	4884853	150	07/28/1988	DRAWER INTERLOCKING MEANS FOR STORAGE CABINET	BOWYER, STEPHEN M.
07516675	Not Issued	163	04/30/1990	GEAR CONFIGURATION FOR STORAGE CABINET DRAWER INTERLOCKING ASSEMBLY	BOWYER, STEPHEN M.

Inventor Search Completed: No Records to Display.

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